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processing capabilities, without requiring encoded throttling control data associated with the video data; and

A1  
controlling computational processing requirements of the decoder, based on the throttling amount, comprising reducing an amount of processing performed on the decoded video data prior to displaying a picture comprising the decoded video data.

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5. (Amended - Clean Copy) A system for dynamically processing incoming video data comprising:

A2  
a video decoder that receives and decodes the video data; and

a decoder throttling device that determines a throttling amount, using at least one of a measure of computational processing power required to decode the video data and a measure of a processing capability of the decoder, without requiring encoded throttling control data;

wherein the decoder throttling device provides the throttling amount to the decoder, which reduces an amount of processing performed on the decoded video data prior to displaying a picture comprising the decoded video data, in accordance with the throttling amount.

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9. (Amended - Clean Copy) A method of reducing processing power requirements of a video decoder that receives and decodes incoming video data, the method comprising:

B3  
determining a throttling amount, using at least one of a measure of computational processing power required to decode at least one bitstream of the video data and a measure of the decoder's processing capabilities, without requiring encoded throttling control data associated with the video data; and

controlling computational processing requirements of the decoder, based on the throttling

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A3  
amount, comprising reducing a number of coefficients inverse quantized and inverse DCT transformed by selectively setting coefficients to alternate values.

12. (Amended - Clean Copy) A system for dynamically processing incoming video data comprising:

A4  
a video decoder that receives and decodes the video data; and

a decoder throttling device that determines a throttling amount, using at least one of a measure of computational processing power required to decode the video data and a measure of a processing capability of the decoder, without requiring encoded throttling control data;

wherein the decoder throttling device provides the throttling amount to the decoder, which reduces a number of coefficients inverse quantized and inverse DCT transformed by selectively setting coefficients to alternate values, in accordance with the throttling amount.

#### REMARKS

Initially, Applicant would like to express his appreciation to the Examiner for allowing claims 14-20 in the present application.

Upon entry of the present amendment, claims 1, 5, 9 and 12 will have been amended to more clearly recite the claimed subject matter and to enhance the clarity of the claim language, as discussed below. Applicants respectfully submit that all pending claims are now in condition for allowance.

In the above-referenced Official Action, the Examiner rejected claims 1, 4, 5 and 8 under 35 U.S.C. § 103(a) as being unpatentable over AGARWAL (U.S. Patent No. 5,812,788) in view of LIU et al. (U.S. Patent No. 5,680,482). The Examiner rejected claims 9-13 under 35 U.S.C. § 103(a) as